

# A General Framework for Sound and Complete Floyd-Hoare Logics

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This paper presents an abstraction of Hoare logic to traced symmetric monoidal categories, a very general framework for the theory of systems. Our abstraction is based on a traced monoidal functor from an arbitrary traced monoidal category into the category of pre-orders and monotone relations. We give several examples of how our theory generalises usual Hoare logics (partial correctness of while programs, partial correctness of pointer programs), and provide some case studies on how it can be used to develop new Hoare logics (run-time analysis of while programs and stream circuits).

Categories and Subject Descriptors: F.3.1 [**Logics and Meanings of Programs**]: Specifying and Verifying and Reasoning about Programs

General Terms: Hoare logic, traced monoidal categories

Additional Key Words and Phrases: Stream circuits

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## 1. INTRODUCTION

Under the general label of *Hoare logic*, the early work of Floyd [1967] and Hoare [1969] on axiom systems for flowcharts and while programs has been applied to various other domains, such as recursive procedures [Apt 1981], pointer programs [Reynolds 2002], and higher-order languages [Berger et al. 2005]. Our goal in this paper is to identify a minimal structure supporting soundness and (relative) completeness results, in the manner of Cook’s presentation for *while programs* [Cook 1978]. This is achieved via an abstraction of Hoare logic to the theory of traced symmetric monoidal categories [Joyal et al. 1996], a very general framework for the theory of systems.

Traced symmetric monoidal categories precisely capture the intrinsic structure of both flowcharts and dynamical systems, namely: sequential and parallel ‘composability’, and feedback (unbounded iteration). In fact, traced symmetric monoidal categories are closely related to Bainbridge’s work [1976] on the duality between flowcharts and networks. The scope of traced symmetric monoidal categories, how-

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This research was supported by EPSRC grants GR/M98340, GR/L48256 and EP/F02309X/1 and by an EPSRC studentship to the third author. Oliva also gratefully acknowledges support of the Royal Society under grant 516002.K501/RH/kk.

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ever, is much broader, being actively used, for instance, for modelling computation (e.g. [Simpson and Plotkin 2000]) and in connection with Girard’s geometry of interaction (e.g. [Haghverdi and Scott 2005]).

The main feature of Hoare logic is the use of assertions  $P, Q$  specifying the input-output behaviour of a program. If  $A$  is a program, then the triple  $\{P\} A \{Q\}$  states that on inputs satisfying  $P$  the program  $A$ , if it terminates, will produce a result which satisfies property  $Q$ . An inherent ordering among assertions, given by the consequence relation, is also available, allowing for the properties of input/output to be refined or relaxed. In particular, if  $\{P\} A \{Q\}$  holds, and  $Q$  logically implies  $Q'$ , then we must also have that  $\{P\} A \{Q'\}$  holds. Abstractly, assertions can be viewed as objects of a *pre-ordered set*, with the logical implication as an instance of an ordering relation. The derived Hoare triple relation  $\{\cdot\} A \{\cdot\}$  can then be viewed as *monotone* binary relation between the points of the pre-order.

More precisely, let  $\mathcal{H}$  be the category of pre-ordered sets and monotone relations (see Section 3 for formal definition). We first identify a particular class of functors – which we call *verification functors* – between traced symmetric monoidal categories and subcategories of  $\mathcal{H}$ . We then give an abstract definition of Hoare triples, parametrised by a verification functor, and prove a single soundness and completeness (in the sense of Cook) theorem for such triples. In the particular case of the traced symmetric monoidal category of while programs (respectively, pointer programs) this embedding gives us back Hoare’s original logic [1969] (respectively, O’Hearn and Reynolds logic [2002]). In order to illustrate the generality of our framework, we also derive new sound and complete Hoare-logic-like rules for the verification of running-time (and hence termination) of programs, and the verification of linear dynamical systems (modelled via stream circuits).

The chief contributions of this paper are as follows: *(i)* The definition of the concept of a verification functor, between traced symmetric monoidal categories and the category  $\mathcal{H}$  (Section 3). *(ii)* An abstraction of Hoare triples in terms of verification functors (Definition 3.3). In general, our abstraction of Hoare logic provides a ‘categorical’ recipe for the development of new (automatically) sound and complete Hoare-logic-like rules for any class of systems having the underlying structure of a traced symmetric monoidal category. Moreover, Hoare logic notions such as expressiveness conditions, relative completeness [Cook 1978] and loop invariants, have a clear cut correspondence to some of our abstract notions. *(iii)* Sound and complete rules for our abstract notion of Hoare triples, over a fixed verification functor (Theorem 3.4). *(iv)* Four concrete instances of our abstraction, namely: partial correctness of while programs, partial correctness of pointer programs (separation logic), run-time analysis of while programs, and stream circuits (Section 4). In Section 6, we discuss the link between our work and other abstractions of Hoare logic.

## 2. SYSTEMS IN THE ABSTRACT

In this section we describe an abstraction of “system” to be used in our abstraction of the Floyd-Hoare logic [Floyd 1967; Hoare 1969]. Consider first the case of flowcharts, or programming languages in general. The essential construct in this case is the unbounded iteration. For instance, in the Figure 1 we have depicted a

flowchart where the factorial of  $y$  is calculated on the variable  $x$  via an iterative process. As suggested in the picture, the iteration corresponds to a feedback loop from the (yes) branch of the boolean test, to the next step in the computation of the factorial. Note that the code inside the loop ( $x := xy; y := y - 1$ ) can be seen as a local process which at each iteration is gearing the computation to achieve the desired result – the computation of the factorial of  $y$ .

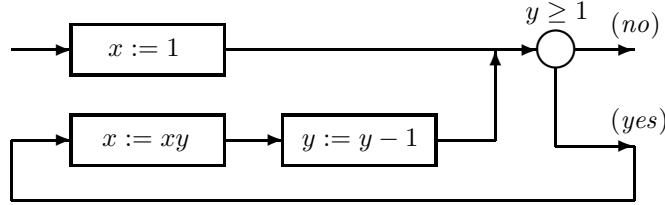


Fig. 1. Example of feedback for flowcharts

Similarly, in the case of continuous (dynamical) systems, the feedback operation is essential for allowing the correction of minor errors and achieving stability. In Figure 2 we depict the control law diagram for a system consisting of a cart (mass  $m$ ) attached to a wall via a spring (constant  $k$ ) being pulled away from the wall via a force  $f$ . This diagrammatic expression of the differential equation  $m\ddot{x} + kx - f = 0$  might serve, for example, as the design for an analogue computer to simulate the mechanical system. Similarly to the example of while programs described above, in this case the spring acts inside the feedback loop ( $-k/m$ ) as a controller, trying to keep the cart within a particular region.

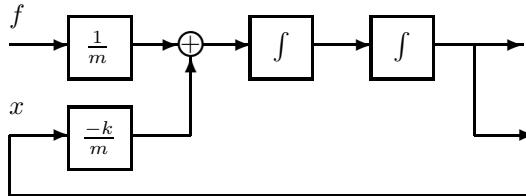


Fig. 2. Example of feedback for dynamic systems

As exemplified above, systems of very different kinds are in general often described via *block diagrams*, with a fixed block-diagram language  $\mathcal{L}$ . Systems in  $\mathcal{L}$  are built from a set  $\mathcal{L}^b$  of basic building blocks via sequential and parallel composition, and feedback (see Figure 3). Let us adopt the convention of using  $A, B, C$  for the syntactic block diagrams.

On closer inspection, one will notice that the main properties of the sequential composition are captured by the basic structure of a category<sup>1</sup>. In order to

<sup>1</sup>For the rest of this article we will assume some basic knowledge of category theory. For a readable introduction see [Mac Lane 1998]. Given a category  $\mathcal{S}$ , we will denote its objects by

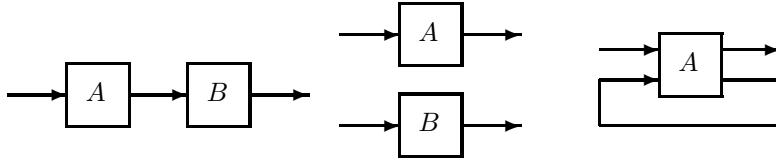


Fig. 3. Block diagram constructors: sequential composition  $A; B$ , parallel composition  $\text{Par}(A, B)$ , and feedback  $\text{Fb}(A)$ , respectively.

capture also the intrinsic properties of the parallel composition  $\text{Par}(A, B)$  and feedback  $\text{Fb}(A)$ , a particular class of categories has been singled out, so-called *traced symmetric monoidal categories*.

Recall that a pair comprising a category  $\mathcal{S}$  and a covariant bifunctor  $\otimes$  is called a *monoidal category* if for some particular object of  $\mathcal{S}$  (the identity element of the monoid)  $\otimes$  satisfies the monoidal axioms of associativity and identity (for details, see [Mac Lane 1998, chapter 11]). A monoidal category is called symmetric if there exists a family of natural isomorphisms  $c_{X,Y} : X \otimes Y \rightarrow Y \otimes X$  satisfying the two braiding axioms plus the symmetry axiom  $c_{X,Y} \circ c_{Y,X} = \text{id}_{X \otimes Y}$ . In [Joyal et al. 1996], the notion of *traced symmetric monoidal category* is introduced<sup>2</sup>, for short *TMC*. A symmetric monoidal category is traced if for any morphism  $f : X \otimes Z \rightarrow Y \otimes Z$  there exists a morphism  $\text{Tr}_{X,Y}^Z(f) : X \rightarrow Y$  satisfying the trace axioms (see [Joyal et al. 1996] for details). We will normally omit the decoration in  $\text{Tr}_{X,Y}^Z$  whenever it is clear over which objects the trace is being applied. Morphisms of a TMC can be represented diagrammatically as input-output boxes, so that, if  $f : X \otimes Z \rightarrow Y \otimes Z$  then  $\text{Tr}(f) : X \rightarrow Y$  corresponds to a *feedback* over the ‘wire’  $Z$ , as shown in Figure 4. In Sections 4 and 5 we will give the formal definition of TMCs based on disjoint union (flowcharts) and cartesian product (networks). Before that we define the necessary concepts for our abstract Hoare logic system.



Fig. 4. Trace diagrammatically

In our work, we are interested in traced monoidal categories which arise as the semantics of a syntactic block diagram language  $\mathcal{L}$ . More precisely, for each block diagram language  $\mathcal{L}$  we will consider (possibly different) semantic mappings  $\llbracket \cdot \rrbracket$

$\mathcal{S}_o$  and its morphisms by  $\mathcal{S}_m$ . Composition between two morphisms will be denoted as usual by  $(g \circ f) : X \rightarrow Z$ , if  $f : X \rightarrow Y$  and  $g : Y \rightarrow Z$ .

<sup>2</sup>In fact, [Joyal et al. 1996] introduces the theory of traces for a more general class of monoidal categories, so-called balanced monoidal categories, of which symmetric monoidal categories are a special case.

into particular traced monoidal categories  $\mathcal{S}$ . We will use  $f, g, h$  for the semantic functions denoted by these block diagrams. We assume that the semantic mapping is compositional, i.e.

$$\begin{aligned}\llbracket A; B \rrbracket &= \llbracket B \rrbracket \circ \llbracket A \rrbracket \\ \llbracket \text{Par}(A, B) \rrbracket &= \llbracket A \rrbracket \otimes \llbracket B \rrbracket \\ \llbracket \text{Fb}(A) \rrbracket &= \text{Tr}_{\mathcal{S}}(\llbracket A \rrbracket).\end{aligned}$$

Note that the objects and morphisms in the range of such a semantic mapping form a sub-TMC of  $\mathcal{S}$  and we can view  $\mathcal{L}$  as a typed language in which the types identify hom-sets in this sub-TMC and where the constructors combine the types of their operands as follows:

$$\begin{aligned}(\cdot; \cdot) &: (\alpha \rightarrow \beta) \times (\beta \rightarrow \gamma) \Rightarrow \alpha \rightarrow \gamma \\ \text{Par}(\cdot, \cdot) &: (\alpha \rightarrow \beta) \times (\gamma \rightarrow \delta) \Rightarrow (\alpha \otimes \gamma) \rightarrow (\beta \otimes \delta) \\ \text{Fb}(\cdot) &: (\alpha \otimes \gamma) \rightarrow (\beta \otimes \gamma) \Rightarrow \alpha \rightarrow \beta.\end{aligned}$$

### 3. ABSTRACT HOARE LOGIC

Recall that a pre-order is a pair  $(X, \leq)$  consisting of a set  $X$  and a binary relation  $\leq$  on  $X$  which is reflexive and transitive. For instance, the set of formulas of a fixed first-order theory under the consequence relation (i.e.  $A \vdash B$ ) forms a pre-order, or any set of sets under subset inclusion  $\subseteq$ .

Let  $A$  be a while program,  $P, Q$  be assertions (formulas) over the program variables of  $A$ , and  $\{P\} A \{Q\}$  denote the usual Hoare triple for while programs. The only property of the binary relation  $\{\cdot\} A \{\cdot\}$  which does not depend on the structure of  $A$ , and hence is intrinsic to the Hoare triples themselves, is the monotonicity stated in the *consequence rule*:

if  $\{P\} A \{Q\}$  holds and  $P' \rightarrow P$  and  $Q \rightarrow Q'$  then  $\{P'\} A \{Q'\}$  also holds.

The following definition captures this basic property of assertions and Hoare triples. When  $r$  is a binary relation we write  $x \underline{r} y$  as a shorthand for  $\langle x, y \rangle \in r$ .

*Definition 3.1 Hoare category.* A relation  $r : X \times Y$  between two pre-ordered sets  $X, Y$  is called *monotone* if  $P \underline{r} Q$  and  $P' \sqsubseteq_X P$  and  $Q \sqsubseteq_Y Q'$  implies  $P' \underline{r} Q'$ . Let  $\mathcal{H}$  denote the category of pre-ordered sets and monotone relations. We call  $\mathcal{H}$  the *Hoare category*.

It is easy to see that  $\mathcal{H}$  can be considered a symmetric monoidal category, with the monoidal operation as cartesian product, since the cartesian product of two pre-ordered sets  $X, Y$  forms again a pre-ordered set with the order on  $X \times Y$  defined coordinatewise, i.e.,  $\langle x, y \rangle \sqsubseteq \langle x', y' \rangle$  iff  $x \sqsubseteq x'$  and  $y \sqsubseteq y'$ . It is also easily verified that the trace based on cartesian product (see Section 5) gives a trace on  $\mathcal{H}$ , making it into a TMC. Given a block diagram language  $\mathcal{L}$ , with its respective trace monoidal semantics, we define an abstract notion of Hoare logic using certain functors from the semantic domains into  $\mathcal{H}$ :

*Definition 3.2 (Verification functor).* Let  $\mathcal{L}$  be a fixed block diagram language with semantics  $\llbracket \cdot \rrbracket : \mathcal{L} \rightarrow \mathcal{S}$ . A mapping  $H : \mathcal{S} \rightarrow \mathcal{H}$  is called a *verification functor*

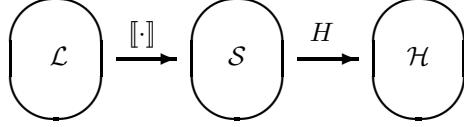


Fig. 5. Block diagram language  $\mathcal{L}$ , tr. mon. semantic  $\mathcal{S}$ , and Hoare category  $\mathcal{H}$ .

if it behaves like a strict traced monoidal functor on the image (a sub-TCM of  $\mathcal{S}$ ) of the semantic mapping  $[\cdot]$  (see Figure 5).

Let a verification functor  $H : \mathcal{S} \rightarrow \mathcal{H}$  be fixed. Each object  $X \in \mathcal{S}_o$  corresponds to a pre-ordered set  $H(X)$ , and each morphism  $f$  in  $\mathcal{S}$  corresponds to a monotone relation  $H(f)$ . Under the assumption that the semantic mapping  $[\cdot]$  is compositional, the verification functor condition is equivalent to:

- (SC1)  $H([\![A; B]\!]) = H([\![B]\!]) \circ H([\![A]\!])$
- (SC2)  $H([\![\text{Par}(A, B)]\!]) = H([\![A]\!]) \times H([\![B]\!])$
- (SC3)  $H([\![\text{Fb}(A)]\!]) = \text{Tr}_{\mathcal{H}}(H([\![A]\!]))$

for all block diagrams  $A, B$  in the block diagram language  $\mathcal{L}$ . Relational equality will be needed for soundness and completeness of the corresponding Hoare logic. As we will see, if only soundness is required only relational inclusion is needed, i.e.

- (S1)  $H([\![A; B]\!]) \supseteq H([\![B]\!]) \circ H([\![A]\!])$
- (S2)  $H([\![\text{Par}(A, B)]\!]) \supseteq H([\![A]\!]) \times H([\![B]\!])$
- (S3)  $H([\![\text{Fb}(A)]\!]) \supseteq \text{Tr}_{\mathcal{H}}(H([\![A]\!]))$

*Definition 3.3 Abstract Hoare Triples.* Let  $A$  be a concrete block diagram, whose meaning is a morphism  $[\![A]\!] : X \rightarrow Y$  in  $\mathcal{S}$ . Moreover, let  $P \in H(X)$  and  $Q \in H(Y)$ . Define *abstract Hoare triples* as

$$\{P\} A \{Q\} := P \underline{H([\![A]\!]}) Q \quad (1)$$

Although we use the same notation as the standard Hoare triple, it should be noted that the meaning of our abstract Hoare triple can only be given once the verification functor  $H$  is fixed. The usual Hoare logic meaning of *if P holds before the execution of A then, if A terminates, Q holds afterwards* will be one of the special cases of our general theory. See Sections 4 and 5 for other meanings of  $\{P\} f \{Q\}$ .

Let a specific block diagram language be fixed, and let  $\mathcal{S}$  be the traced monoidal category denoting the programs of the given language. Moreover, let  $H : \mathcal{S} \rightarrow \mathcal{H}$  be a fixed verification functor. We will denote by  $\mathbf{HL}(\mathcal{L}, [\cdot], H)$  the set of rules shown in Figure 6, where the side conditions are:

- (†)  $A \in \mathcal{L}^b$  and  $P \underline{H([\![A]\!]}) Q$
- (‡)  $P' \sqsubseteq P$  and  $Q \sqsubseteq Q'$

|   |  |
|---|--|
| $\frac{}{\{P\} A \{Q\}} \text{(Ax)} (\dagger)$  | $\frac{\{P\} A \{Q\}}{\{P'\} A \{Q'\}} \text{(Con)} (\ddagger)$                                    |
| $\frac{\{P\} A \{Q\} \quad \{R\} B \{S\}}{\{\langle P, R \rangle\} \text{Par}(A, B) \{\langle Q, S \rangle\}} \text{(Par)}$ |  |
| $\frac{\{P\} A \{Q\} \quad \{Q\} B \{R\}}{\{P\} A; B \{R\}} \text{(Seq)}$   | $\frac{\{\langle P, Q \rangle\} A \{\langle R, Q \rangle\}}{\{P\} \text{Fb}(A) \{R\}} \text{(Fb)}$ |

Fig. 6. The system  $\mathbf{HL}(\mathcal{L}, \llbracket \cdot \rrbracket, H)$  with  $\llbracket \cdot \rrbracket : \mathcal{L} \rightarrow \mathcal{S}$  and  $H : \mathcal{S} \rightarrow \mathcal{H}$ .

The formal system  $\mathbf{HL}(\mathcal{L}, \llbracket \cdot \rrbracket, H)$  should be viewed as a *syntactic* axiomatisation<sup>3</sup> of the ternary relation  $\{P\} A \{Q\}$ . The verification functor  $H$  gives the *semantics* of the Hoare triples (and rules). By soundness and completeness of the system  $\mathbf{HL}(\mathcal{L}, \llbracket \cdot \rrbracket, H)$  we mean that syntax corresponds precisely to semantics, i.e. a syntactic Hoare triple  $\{P\} A \{Q\}$  is provable in  $\mathbf{HL}(\mathcal{L}, \llbracket \cdot \rrbracket, H)$  if and only if  $P \underline{H(\llbracket A \rrbracket)} Q$  is true in  $\mathcal{H}$ .

**THEOREM 3.4** SOUNDNESS AND COMPLETENESS. *The system  $\mathbf{HL}(\mathcal{L}, \llbracket \cdot \rrbracket, H)$  is sound and complete.*

**Proof.** Soundness is trivially true for the axioms. The consequence rule is sound by the monotonicity of the relation  $H(\llbracket A \rrbracket)$ . Soundness of the composition rule also uses the fact that  $H$  respects composition, i.e.

$$P \underline{H(\llbracket A; B \rrbracket)} R \Leftrightarrow P \underline{H(\llbracket B \rrbracket)} \circ H(\llbracket A \rrbracket) R.$$

Soundness of the cartesian product rule uses that the functor  $H$  is monoidal, i.e.

$$\langle P, Q \rangle \underline{H(\llbracket A \otimes B \rrbracket)} \langle R, S \rangle \Leftrightarrow \langle P, Q \rangle \underline{H(\llbracket A \rrbracket)} \times H(\llbracket B \rrbracket) \langle R, S \rangle.$$

For the soundness of the trace rule, assume  $\langle P, Q \rangle \underline{H(\llbracket A \rrbracket)} \langle R, Q \rangle$ . By the definition of the trace on  $\mathcal{H}$  we have  $P \underline{\text{Tr}(H(\llbracket A \rrbracket))} R$ . Finally, by the fact that  $H$  is traced we have  $P \underline{H(\llbracket \text{Fb}(A) \rrbracket)} R$ . We argue now about completeness. It is easy to verify that all true statements of the form  $\{P\} A \{Q\}$ , for basic diagrams  $A$ , are provable. It remains to show that if  $\{P\} A \{Q\}$  is true, for an arbitrary block diagram  $A$ , then there exists a premise of the corresponding rule (depending on the structure of  $A$ ) which is also true. If  $\{P\} A; B \{R\}$  is true then, since  $H$  respects composition, there must exist a  $Q$  such that both

$$P \underline{H(\llbracket A \rrbracket)} Q \quad \text{and} \quad Q \underline{H(\llbracket B \rrbracket)} R$$

<sup>3</sup>Note that assertions  $P, Q, R$  could potentially be semantic objects (e.g. sets), which is harmless since the only structure required from assertions is the ability to form a pair  $\langle P, Q \rangle$  out of two assertions  $P$  and  $Q$ . Moreover, the fact that logical axioms have a semantic side condition  $(\dagger)$  is not too restrictive since this is only assumed for the basic block diagrams, and these are normally manageable computationally.

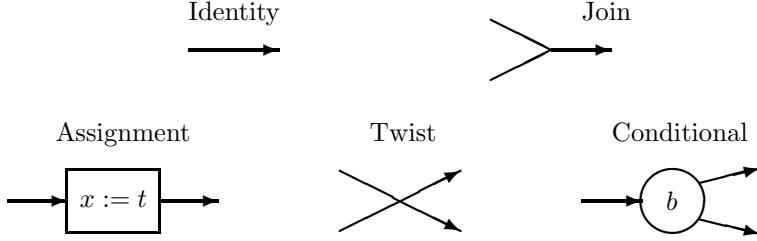


Fig. 7. Basic flowcharts.

are true. If  $\{\langle P, R \rangle\} A \otimes B \{\langle Q, S \rangle\}$  is true then so it is  $\{P\} A \{Q\}$  and  $\{R\} B \{S\}$ , by the fact that  $H$  is monoidal. Finally, if  $\{P\} \text{Fb}(A) \{R\}$  is true, then so is

$$\langle P, Q \rangle \underline{H(\llbracket A \rrbracket)} \langle R, Q \rangle,$$

for some  $Q$  by the definition of a verification functor.  $\square$

The abstract proof of soundness and completeness presented above is both short and simple, using only the assumption of a verification functor and basic properties of the category  $\mathcal{H}$ . As we will see, the laborious work is pushed into showing that  $H$  is a verification functor. That will be clear in Section 4.3, where we build a verification functor appropriate for while programs, using Cook's expressiveness condition [Cook 1978].

#### 4. VERIFICATION FUNCTORS FOR FLOWCHARTS

The traced monoidal categories that correspond to classical Hoare logics for imperative programs are based on the category of sets and relations with the monoidal structure arising from the disjoint union operator  $\uplus$ . We will write  $\text{inj}_i : X_i \rightarrow X_0 \uplus X_1$  for the injections of the summands into a disjoint union. In this TMC, the trace of a relation  $f : X \uplus Z \rightarrow Y \uplus Z$  is then defined as follows:

$$x \underline{\text{Tr}}(f) y := \exists z_0 \dots z_n (\text{inj}_0 x \underline{f} \text{inj}_1 z_0 \wedge \dots \text{inj}_1 z_i \underline{f} \text{inj}_1 z_{i+1} \dots \wedge \text{inj}_1 z_n \underline{f} \text{inj}_0 y)$$

This trace based on disjoint union lets us view feedback loops as representing iterative processes.

First we will consider systems (block diagrams) where disjoint union is used for the monoidal structure in the semantic domains. These are normally called “flowcharts”, and two instances of these are (refinements of) while programs and pointer programs. The instantiations will produce the original Hoare logic [Hoare 1969], separation logic and a new Hoare logic for running time analysis.

##### 4.1 The programming language: flowcharts

In the case of simple diagrammatic while programs the basic constructs of our language  $\mathcal{L}_{\text{FC}}$  are shown in Figure 7. These can be put together via sequential composition, parallel composition and feedback, as shown in Figure 3. We will consider an extension  $\mathcal{L}_{\text{PP}}$  of this programming language with pointers by adding the basic pointer programs shown in Figure 8.

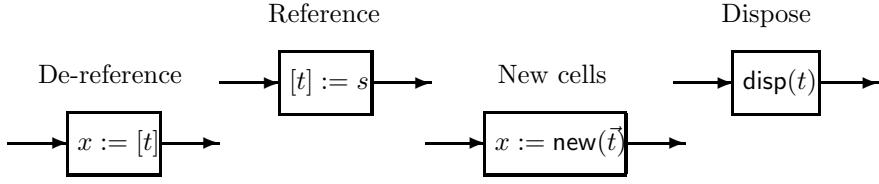


Fig. 8. Basic pointer programs.

#### 4.2 The semantics

The programs (flowcharts) which can be built as described in Section 4.1 can be given a denotation as follows. Let  $\text{Store}$  be the set mappings  $\rho : \text{Var} \rightarrow \mathbb{Z}$  assigning an integer value to each of the program variables  $\text{Var} = \{x, y, \dots\}$ . Let the objects of  $\mathcal{S}_{\text{FC}}$  be the set (of sets) containing the empty set  $\emptyset$  and  $\text{Store}$ , and closed under *disjoint union*, i.e.  $\{\emptyset, \text{Store}, \text{Store} \uplus \text{Store}, \dots\}$ . Consider also the following family of functions between the objects of  $\mathcal{S}_{\text{FC}}$ :

- *Skip, id* :  $\text{Store} \rightarrow \text{Store}$   
 $\text{id}(\rho) := \rho$
- *Assignment,  $(x := t)$*  :  $\text{Store} \rightarrow \text{Store}$   
 $(x := t)(\rho) := (\rho)[t_\rho/x]$
- *Joining,  $\Delta$*  :  $\text{Store} \uplus \text{Store} \rightarrow \text{Store}$   
 $\Delta(\text{inj}_i(\rho)) := \rho$
- *Twist,  $c$*  :  $\text{Store} \uplus \text{Store} \rightarrow \text{Store} \uplus \text{Store}$   
 $\text{twist}(\langle v, \rho \rangle) := \langle \overline{v}, \rho \rangle$
- *Forking,  $\nabla_b$*  :  $\text{Store} \rightarrow \text{Store} \uplus \text{Store}$   
 $\nabla_b(\rho) := \begin{cases} \text{inj}_0(\rho) & \text{if } \neg b_\rho \\ \text{inj}_1(\rho) & \text{otherwise} \end{cases}$

The conditional forking ( $\nabla_b$ ) and the assignment ( $x := t$ ) are parametrised by functions  $b$  and  $t$  (intuitively, expressions) from  $\text{Store}$  to the boolean lattice  $\mathbb{B}$  and  $\mathbb{Z}$ , respectively, so that  $b_\rho$  and  $t_\rho$  denote their value on a given store  $\rho$ . We use  $\text{inj}_0$  and  $\text{inj}_1$  for left and right injections into  $\text{Store} \uplus \text{Store}$ .

We then close the set of basic functions in  $\mathcal{S}_{\text{FC}}$  under sequential composition of functions, disjoint union of functions and the standard trace for disjoint union, to form the set of partial functions which are the morphisms of  $\mathcal{S}_{\text{FC}}$ . It is easy to see that  $\mathcal{S}_{\text{FC}}$  forms a TMC.

In the case of pointer programs, we consider an adaptation  $\mathcal{S}_{\text{FC}}$  of the above TMC to a category of programs that manipulate both stores and heaps, which we will refer to as the traced symmetric monoidal category of *pointer programs*  $\mathcal{S}_{\text{PP}}$ . Let  $\text{State} := \text{Store} \times \text{Heap}$ , where  $\text{Store}$  is as above and  $\text{Heap}$  is the set of partial functions (from  $\mathbb{N}$  to  $\mathbb{Z}$ ) with finite domain. Define also for any set  $X$  a new set  $X_a$  as  $X \cup \{\text{abort}\}$ . We view the elements of the heap as pairs consisting of a function  $h : \mathbb{N} \rightarrow \mathbb{Z}$  and a finite set  $d \in \mathcal{P}_{\text{fin}}(\mathbb{N})$  describing the valid domain of  $h$ .

We then form the set (of sets) of objects of  $\mathcal{S}_{\text{PP}}$  as the set containing the empty set  $\emptyset$  and  $\text{State}_a$ , and closed under *disjoint union* but maintaining `abort` as a form of bottom element, i.e.  $\{\emptyset, \text{State}_a, (\text{State} \uplus \text{State})_a, \dots\}$ . It is easy to check that  $\mathcal{S}_{\text{PP}}$  is also a TMC. Each of the basic functions of  $\mathcal{S}_{\text{FC}}$  can be lifted to the extended type structure, by simply ignoring the ‘heap’ component, or by propagating `abort` when receiving an `abort` as input. The set of basic functions of  $\mathcal{S}_{\text{PP}}$  is an extension of the set of (the lifting of the) basic functions of  $\mathcal{S}_{\text{FC}}$  with the following family of functions:

- *Look up*,  $(x := [t]) : \text{State}_a \rightarrow \text{State}_a$   
 $(x := [t])(\rho, h, d) := \begin{cases} (\rho[h(t_\rho)/x], h, d) & t_\rho \in d \\ \text{abort} & \text{otherwise} \end{cases}$
- *Mutation*,  $([t] := s) : \text{State}_a \rightarrow \text{State}_a$   
 $([t] := s)(\rho, h, d) := \begin{cases} (\rho, h[t_\rho \mapsto s_\rho], d) & t_\rho \in d \\ \text{abort} & \text{otherwise} \end{cases}$
- *Allocation*,  $x := \text{new}(\vec{t}) : \text{State}_a \rightarrow \text{State}_a$   
 $(x := \text{new}(\vec{t}))(\rho, h, d) := (\rho[x \mapsto i], h[i + j \mapsto t_j], d \uplus \{i, \dots, i + n\})$
- *Deallocation*,  $\text{disp}(t) : \text{State}_a \rightarrow \text{State}_a$   
 $(\text{disp}(t))(\rho, h, d) := \begin{cases} (\rho, h, d \setminus \{t_\rho\}) & t_\rho \in d \\ \text{abort} & \text{otherwise} \end{cases}$

In order to make the allocation functional deterministic, we let  $i$  be the address location succeeding the maximum address already defined.

As done in the case of flowcharts above, we can then close the set of basic functions under sequential composition, disjoint union and trace, to form the set of partial functions which are the morphisms of  $\mathcal{S}_{\text{PP}}$ . We are assuming that the functions are strict with respect to `abort`, i.e. on the `abort` state all programs will return `abort`. The category  $\mathcal{S}_{\text{PP}} \equiv (\mathcal{S}_{\text{PP}}, \uplus, \text{Tr})$ , with the standard trace for disjoint union, forms another example of a TMC with the extra basic morphisms look up, mutation, allocation and deallocation.

#### 4.3 Hoare logic for partial correctness

In this section we present a verification embedding of the TMC of flowcharts  $\mathcal{S}_{\text{FC}}$ . This will give us soundness and (relative) completeness of Hoare’s original verification logic [Hoare 1969] for partial correctness (using forward reasoning).

Let us now define the monoidal functor  $H_{\text{FC}} : \mathcal{S}_{\text{FC}} \rightarrow \mathcal{H}$ . Let a Cook-expressive<sup>4</sup> first-order theory be fixed. On the objects  $X \in \mathcal{S}_{\text{FC}}$  we let  $H_{\text{FC}}(X)$  be a pre-ordered set of formulas. The ordering  $P \sqsubseteq R$  on the elements of  $H_{\text{FC}}(X)$  is taken to be  $P \rightarrow R$  in the fixed theory. We assume that pairs of formulas are also considered formulas, with the connectives defined pointwise. We now define the functor  $H_{\text{FC}}$  on the image of the semantic embedding  $\llbracket \cdot \rrbracket$ . For each flowchart  $A$  we let  $H_{\text{FC}}(\llbracket A \rrbracket)$  be the following monotone relation

<sup>4</sup>Recall that a logic is Cook-expressive if for any program  $f$  and pre-condition  $P$  the strongest post-condition of  $f$  under  $P$  is expressible by a formula in  $\mathcal{L}$  (cf. [Cook 1978]).

$$P \underline{H_{\text{FC}}}(\llbracket A \rrbracket) Q := \text{SPC}(A, P) \rightarrow Q$$

where  $\text{SPC}(A, P)$  is a formula expressing the strongest post-condition of  $A$  under  $P$ . Such formula exists by our assumption that the theory is Cook-expressive. Moreover, note that the denotation of  $\underline{H_{\text{FC}}}(\llbracket A \rrbracket)$ , since it only depends on  $A$  via  $\text{SPC}(A, \cdot)$ , does not depend on the particular syntax of  $A$ , but only on the input-output behaviour of  $A$ , i.e.  $\llbracket A \rrbracket$ . It is also easy to see what  $\text{SPC}$  is for the basic morphisms of  $\mathcal{S}_{\text{FC}}$

$$\begin{aligned} \text{SPC}(\text{id}, P) &:= P \\ \text{SPC}(x := t, P) &:= \exists v(P[v/x] \wedge x = t[v/x]) \\ \text{SPC}(\nabla_b, P) &:= \langle P \wedge \neg b, P \wedge b \rangle \\ \text{SPC}(\text{twist}, \langle P, R \rangle) &:= \langle R, P \rangle \\ \text{SPC}(\Delta, \langle P, R \rangle) &:= P \vee R \end{aligned}$$

The functor  $H$  is monoidal because a formula  $P$  describing a subset of  $X_0 \uplus X_1$  can be seen as a pair of formulas  $\langle P_0, P_1 \rangle$  such that each  $P_i$  describes a subset of  $X_i$ , i.e.  $H_{\text{FC}}(X \uplus Y)$  is isomorphic to  $H_{\text{FC}}(X) \times H_{\text{FC}}(Y)$ . Similarly, there is a one-to-one correspondence between strongest post-condition transformer for a parallel composition of flowcharts  $f \uplus g$  and pairs of predicate transformers  $H_{\text{FC}}(f) \times H_{\text{FC}}(g)$ .

We argue now in two steps that  $H_{\text{FC}}$  is also a verification functor. The main task is to show that  $H_{\text{FC}}$  respects the trace structure, i.e.

$$P \underline{H_{\text{FC}}}(\text{Tr}(\llbracket A \rrbracket)) R \Leftrightarrow P \text{Tr}(H_{\text{FC}}(\llbracket A \rrbracket)) R.$$

By the definition of trace on the  $\mathcal{H}$  we get

$$P \underline{H_{\text{FC}}}(\text{Tr}(\llbracket A \rrbracket)) R \Leftrightarrow \exists Q (\langle P, Q \rangle \underline{H_{\text{FC}}}(\llbracket A \rrbracket) \langle R, Q \rangle)$$

and by the definition of  $H_{\text{FC}}$  above:

$$\text{SPC}(\text{Tr}(\llbracket A \rrbracket))(P) \rightarrow R \Leftrightarrow \exists Q (\text{SPC}(\llbracket A \rrbracket) \langle P, Q \rangle \rightarrow \langle R, Q \rangle).$$

The next lemma proves the left to right implication. The implication from right to left is proven in Theorem 4.2.

**LEMMA 4.1** [COOK 1978]. *Let  $A$  be a block diagram such that  $\llbracket A \rrbracket : X \uplus Z \rightarrow Y \uplus Z$ . Assume  $\vec{z}$  are all the program variables of  $A$ . Moreover, let  $P \in H_{\text{FC}}(X)$  and  $R \in H_{\text{FC}}(Y)$  be fixed formulas. If  $\text{SPC}(\text{Tr}(\llbracket A \rrbracket), P) \rightarrow R$  then  $\text{SPC}(\llbracket A \rrbracket, \langle P, Q \rangle) \rightarrow \langle R, Q \rangle$ , for some formula  $Q$ .*

**Proof.** We construct a formula  $Q$  which is a fixed point for  $\llbracket A \rrbracket$  on  $P$ , i.e.  $\text{SPC}(\llbracket A \rrbracket, \langle P, Q \rangle) \leftrightarrow \langle R', Q \rangle$ , for some formula  $R'$ . We also argue that

$$\text{SPC}(\text{Tr}(\llbracket A \rrbracket), P) \leftrightarrow R'.$$

By our hypothesis  $\text{SPC}(\text{Tr}(\llbracket A \rrbracket), P) \rightarrow R$  it then follows that  $R'$  implies  $R$ , as desired. The fixed point  $Q$  is essentially the strongest loop invariant, and is constructed as follows. Given the block diagram  $A$  we build a new block diagram  $A' : X \uplus Z \rightarrow Y \uplus Z \uplus Z$  where the internal states of  $Z$  can be observed, even after the feedback is applied. Let  $A' := (\text{id} \uplus \nabla_{\vec{z}=\vec{y}}) \circ A$  (see Figure 9) where  $\vec{z}$  is the finite sequence of variables mentioned in the description of  $A$ , and  $\vec{y}$  is

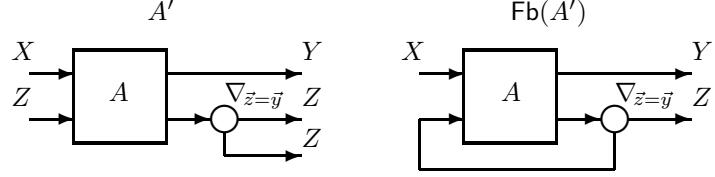


Fig. 9. Cook's construction (forward reasoning)

a fresh tuple of variables of same length. Notice that the program  $\mathsf{Fb}_{X,Y \uplus Z}^Z(A')$  behaves almost as  $\mathsf{Fb}_{X,Y}^Z(A)$  except that the block diagram  $\mathsf{Fb}(A')$  will ‘terminate’ earlier (meaning that the fixed point sequence is shorter) if the state  $\vec{z}$  matches  $\vec{y}$ . Let  $\mathsf{SPC}(\llbracket \mathsf{Fb}(A') \rrbracket, P) = \langle Q_0, Q_1(\vec{y}) \rangle$ . It is easy to see that the formula  $Q \equiv \exists \vec{y} Q_1(\vec{y})$  characterises the possible internal states  $\vec{z}$  in any run of  $\mathsf{Fb}(A)$  on an input satisfying  $P$ . In other words,  $Q$  is the least fixed point for  $H_{\mathsf{FC}}(\llbracket A \rrbracket)$  on  $P$ ,  $\mathsf{SPC}(\llbracket A \rrbracket, \langle P, Q \rangle) \leftrightarrow \langle R', Q \rangle$ , for some formula  $R'$ , which implies  $\mathsf{SPC}(\mathsf{Tr}(\llbracket A \rrbracket), P) \leftrightarrow R'$ .  $\square$

THEOREM 4.2.  $H_{\mathbb{FC}} : \mathcal{S}_{\mathbb{FC}} \rightarrow \mathcal{H}$ , as defined above, is a verification functor for morphisms arising from block diagrams in  $\mathcal{L}_{\mathbb{FC}}$ .

**Proof.** By Lemma 4.1, it remains to be shown that whenever

(i)  $\text{SPC}(\llbracket A \rrbracket, \langle P, Q \rangle) \rightarrow \langle R, Q \rangle$ ,

for a formula  $Q$ , then  $\text{SPC}(\text{Tr}(\llbracket A \rrbracket), P) \rightarrow R$ . Assume (i) and  $\text{SPC}(\text{Tr}(\llbracket A \rrbracket), P)(\rho)$ , for some store value  $\rho$ . We must show  $R(\rho)$ . By the definition of the strongest post-condition there exists a sequence of stores  $\rho', \rho_0, \dots, \rho_n$  such that  $P(\rho')$  and

$$\langle 0, \rho' \rangle \llbracket A \rrbracket \langle 1, \rho_0 \rangle, \dots, \langle 1, \rho_k \rangle \llbracket A \rrbracket \langle 1, \rho_{k+1} \rangle, \dots, \langle 1, \rho_n \rangle \llbracket A \rrbracket \langle 0, \rho \rangle.$$

By a simple induction, using the assumption (i), we get that all  $\rho_k$  satisfy  $Q$  and that  $\rho$  satisfies  $R$ , as desired.  $\square$

Given the semantic embedding  $[\cdot] : \mathcal{L}_{\text{FC}} \rightarrow \mathcal{S}_{\text{FC}}$ , the system  $\mathbf{HL}(\mathcal{L}_{\text{FC}}, [\cdot], H_{\text{FC}})$  obtained via our embedding  $H_{\text{FC}}$  is a refinement of the system given by Hoare [1969], i.e. Hoare's rules are derivable from ours. See, for instance, the case of the while loop rule in Figure 10, given that a while loop  $\text{while}_b(A)$  can be represented in  $\mathcal{S}_{\text{FC}}$  as  $\text{Fb}(\Delta; \nabla_b; (\text{id} \uplus A))$ . Moreover, the soundness and (relative) completeness of the Hoare logic rules for while programs follow easily from Theorems 3.4 and 4.2.

## 4.4 Separation logic

In the case of the extended flowchart language  $\mathcal{L}_{\text{PP}}$  (pointer programs) and its respective semantics, we can define a verification functor  $H_{\text{PP}}$  as follows. On the objects  $X \in \mathcal{S}_{\text{PP}}$  we let  $H_{\text{PP}}(X)$  be a pre-ordered set of formulas with enough primitives to describe the weakest pre-condition of programs, but without `abort` as an atomic formula (we want `abort` not to be expressible in the language). The ordering  $P \sqsubseteq R$  on the elements of  $H_{\text{PP}}(X)$  is again taken to be  $P \rightarrow R$  in the fixed theory. For each pointer program  $A$  we let  $H_{\text{PP}}(\llbracket A \rrbracket)$  be the following monotone relation

$$\begin{array}{c}
\frac{(\Delta \in \mathcal{L}_0^b) \quad (\nabla_b \in \mathcal{L}_0^b) \quad (\text{id} \in \mathcal{L}_0^b)}{\{\langle P, P \rangle\} \Delta \{P\} \quad \frac{\{P\} \nabla_b \{\langle P \wedge \neg b, P \wedge b \}\} \quad \frac{\{P \wedge \neg b\} \text{id} \{P \wedge \neg b\} \quad \{\mathbf{P} \wedge \mathbf{b}\} \mathbf{A} \{\mathbf{P}\}}{\{\langle P \wedge \neg b, P \wedge b \}\} \text{Par}(\text{id}, A) \{\langle P \wedge \neg b, P \rangle\}} \\
\frac{\{\langle P, P \rangle\} \Delta; \nabla_b; \text{Par}(\text{id}, A) \{\langle P \wedge \neg b, P \rangle\}}{\{\langle P, P \rangle\} \Delta; \nabla_b; \text{Par}(\text{id}, A) \{P \wedge \neg b\}} \text{(Fb)} \\
\frac{\{\langle P, P \rangle\} \Delta; \nabla_b; \text{Par}(\text{id}, A) \{P \wedge \neg b\}}{\{\mathbf{P}\} \text{while}_b(\mathbf{A}) \{\mathbf{P} \wedge \neg b\}} \text{(def)}
\end{array}$$

Fig. 10. Derivation of Hoare's while loop rule in  $\mathbf{HL}(\mathcal{L}_{\mathbb{FC}}, \llbracket \cdot \rrbracket, H_{\mathbb{FC}})$ 

$$P \underline{H_{\mathbb{PP}}(\llbracket A \rrbracket)} Q := P \rightarrow \text{WPC}(A, Q)$$

where  $\text{WPC}(A, P)$  is a formula expressing the weakest pre-condition of program  $A$  under post-condition  $Q$ . As in Reynolds [2002], well-specified programs do not abort, since formulas in  $H_{\mathbb{PP}}(X)$  never hold true for `abort`.

It has been shown in [O'Hearn et al. 2001; Reynolds 2002], that the weakest liberal pre-conditions for the new basic statements can be concisely expressed in *separation logic* as (see [Reynolds 2002] for notation)

$$\begin{array}{ll}
\text{WPC}(x := [t], P) & := \exists v'((t \mapsto v') * ((t \mapsto v') \multimap P[v'/x])) \\
\text{WPC}([t] := s, P) & := (t \mapsto -) * ((t \mapsto s) \multimap P) \\
\text{WPC}(x := \text{new}(\vec{t}), P) & := \forall i((i \mapsto \vec{t}) \multimap P[i/x]) \\
\text{WPC}(\text{disp}(t), P) & := (t \mapsto -) * P
\end{array}$$

Similarly to Lemma 4.1 and Theorem 4.2, one can show that the  $H_{\mathbb{PP}}$  defined above is a verification functor. The system  $\mathbf{HL}(\mathcal{L}_{\mathbb{PP}}, \llbracket \cdot \rrbracket, H_{\mathbb{PP}})$ , which we then obtain from our abstract approach is basically the one presented in Reynolds [2002], for *global backward reasoning*, using the logical theory of bunched implications as an ‘oracle’ for the consequence rule.

#### 4.5 Hoare logic for running time analysis and termination

Finally, we conclude this section with a new Hoare logic for *running time* analysis (and *termination*) of programs. For this we need a finer semantic model  $\mathcal{S}_{\mathbb{FC}}^T$  for the flowchart programs. We need a model that distinguishes programs with different running time. Let us consider the identity operation and twist as neutral. We define the running time of a program to be the number of times non-neutral basic operations (e.g. assignment or conditional) are evaluated. Then two programs are identified if they have the same input-output behaviour and same running time on all terminating inputs. This gives rise to a refinement of the semantic model  $\mathcal{S}_{\mathbb{FC}}$  which we will call  $\mathcal{S}_{\mathbb{FC}}^T$ .

Let  $\mathbb{N}^\infty$  denote the usual pre-order of the natural numbers extended with  $\infty$  as the top element, i.e.  $n \leq \infty$  for all  $n \in \mathbb{N}$ . Arithmetic with  $\infty$  is done as usual, e.g.  $n + \infty = \infty$ . We want to use  $\mathbb{N}$  as a counter for the number of steps

$$\begin{array}{c}
\frac{(\nabla_b \in \mathcal{L}_0^b) \quad \{\mathbf{P}\} \mathbf{A} \{(b)?(\mathbf{P}, \mathbf{Q}) + 1\}}{\{(b)?(P, Q) + 1\} \nabla_b \{\langle P, Q \rangle\} \quad \{\langle P, Q \rangle\} \mathbf{Par}(\mathbf{id}, A) \{\langle P, (b)?(P, Q) + 1 \rangle\}} \\
\hline
\frac{\{(b)?(P, Q) + 1\} \nabla_b; \mathbf{Par}(\mathbf{id}, A) \{\langle P, (b)?(P, Q) + 1 \rangle\}}{\{\langle (b)?(P, Q) + 1, (b)?(P, Q) + 1 \rangle\} \Delta; \nabla_b; \mathbf{Par}(\mathbf{id}, A) \{\langle P, (b)?(P, Q) + 1 \rangle\}} \text{ (Fb)} \\
\hline
\frac{\{(b)?(P, Q) + 1\} \mathbf{Fb}(\Delta; \nabla_b; \mathbf{Par}(\mathbf{id}, A)) \{P\}}{\{(b)?(\mathbf{P}, \mathbf{Q}) + 1\} \mathbf{while}_b(\mathbf{A}) \{\mathbf{P}\}} \text{ (def)}
\end{array}$$

Fig. 11. While loop rule for running time

a program takes to terminate. Non-terminating executions are associated with  $\infty$  which should be thought of as “infinite time”. For any set  $\Sigma^m (\equiv \Sigma \uplus \dots \uplus \Sigma)$  the pre-order on  $\mathbb{N}^\infty$  induces a pointwise pre-order on the set of functions  $\Sigma^m \rightarrow \mathbb{N}^\infty$ . We take these pre-ordered sets  $\{\Sigma^m \rightarrow \mathbb{N}^\infty\}_{m \in \mathbb{N}}$  as the objects of our category, with monotone relations between these as morphisms. This forms a subcategory of  $\mathcal{H}$  with cartesian product as the monoidal structure.

Let  $P, Q$  be functions in  $\Sigma^n \rightarrow \mathbb{N}^\infty$  and  $\Sigma^m \rightarrow \mathbb{N}^\infty$ , respectively. Note that  $P$  for instance can be viewed as an  $n$ -tuple  $(P_1, \dots, P_n)$  of functions  $P_i : \Sigma \rightarrow \mathbb{N}^\infty$ . Given a flowchart  $A$  (assume  $\llbracket A \rrbracket : \Sigma^n \rightarrow \Sigma^m$ ) we define the following functor

$$P \underline{H_{\mathbb{FC}}^T}(\llbracket A \rrbracket) Q := P \geq \mathbf{RRT}(A, Q)$$

where  $\mathbf{RRT}(A, Q) : \Sigma^n \rightarrow \mathbb{N}^\infty$  calculates the *relative running time* of  $A$  with respect to  $Q$ . Intuitively, if  $A$  does not terminate on input  $\rho$  then  $\mathbf{RRT}(A, Q)(\rho) = \infty$ , otherwise  $\mathbf{RRT}(A, Q)(\rho)$  calculates  $\mathbf{RunTime}(A, \rho) + Q(\llbracket A \rrbracket(\rho))$ . This can be given for the basic programs as

$$\begin{array}{ll}
\mathbf{RRT}(\mathbf{id}, Q) & := Q \\
\mathbf{RRT}(x := t, Q) & := Q[t/x] + 1 \\
\mathbf{RRT}(\nabla_b, \langle P, Q \rangle) & := (b)?(P, Q) + 1 \\
\mathbf{RRT}(\mathbf{twist}, \langle P, Q \rangle) & := \langle Q, P \rangle \\
\mathbf{RRT}(\Delta, Q) & := \langle Q + 1, Q + 1 \rangle
\end{array}$$

As usual, in order to obtain a completeness result we assume that the language over which we are defining our pre- and post-conditions is rich enough to express  $\mathbf{RRT}$ . Given that  $\mathbf{RRT}(A, 0)(\rho) = \infty$  implies the program  $A$  does not terminate on input  $\rho$ , we can conclude that any expressive language will need to include non-computable functions. This is to be expected, since an expressive language where each function is computable would in this case allow us to solve the halting problem. It is easy to see, however, that extending a Turing complete language with an oracle for the halting problem is sufficient for obtaining an expressive language.

As in Definition 3.3, the functor  $\underline{H_{\mathbb{FC}}^T}$  gives rise to a Hoare triple  $\{P\} A \{Q\}$ . The relation  $\{P\} A \{Q\}$  holds if and only if on each input  $\rho$  such that  $P(\rho) \neq \infty$  then  $Q \neq \infty$  and  $A$  on  $\rho$  runs in time  $P(\rho) - Q(\llbracket A \rrbracket(\rho))$  (this in particular implies that the program terminates).

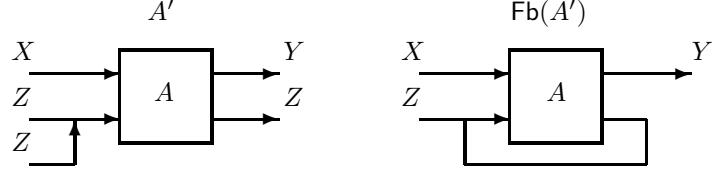


Fig. 12. Cook's construction (backward reasoning)

THEOREM 4.3.  $H_{\text{FC}}^T : \mathcal{S}_{\text{FC}}^T \rightarrow \mathcal{H}$  as defined above is a verification functor.

**Proof.** First of all, it is easy to check that  $H_{\text{FC}}^T$  indeed maps objects and morphisms of  $\mathcal{S}_{\text{FC}}^T$  into pre-orders and monotone relations (objects and morphisms of  $\mathcal{H}$ ).  $H_{\text{FC}}^T$  also trivially satisfies conditions (SC1) and (SC2), i.e.  $H_{\text{FC}}^T$  is a monoidal functor. One direction of condition (SC3) is similar to the proof of Lemma 4.1. Given a flowchart  $A$  we construct a new flowchart  $A'$  (see Figure 12) in such way that  $\text{RRT}(\text{Fb}(A'), R)$  gives us the (greatest) fixed point of  $A$ . The other direction of condition (SC3) is similar to the proof of Theorem 4.2.  $\square$

Therefore, this gives rise to a Hoare logic system  $\text{HL}(\mathcal{L}_{\text{FC}}, \llbracket \cdot \rrbracket, H_{\text{FC}}^T)$  which can be used to prove upper bounds on the running time of a program. As a simple example, consider the program calculating the factorial of  $x$  described in Section 2 (Figure 1):

$$\begin{array}{c}
 \frac{\{3y\} x := xy \{3y - 1\} \quad \{3y - 1\} y := y - 1 \{3y + 1\}}{\{3y\} x := xy; y := y - 1 \{3y + 1\}} \\
 \hline
 \{3y + 2\} x := 1 \{3y + 1\} \quad \{3y + 1\} \text{while}_b(x := xy; y := y - 1) \{3y\} \\
 \hline
 \{3y + 2\} (x := 1); \text{while}_b(x := xy; y := y - 1) \{3y\}
 \end{array}$$

where  $b \equiv (y \geq 1)$ . Given that  $y = 0$  at the end, we have a proof of

$$\{3x - 1\} (y := x); \text{while}_b(y := y - 1; x := xy) \{0\},$$

which gives an upper bound of  $3x - 1$  steps on the running time of the program.

## 5. VERIFICATION FUNCTORS FOR NETWORKS

We will now apply our approach to Hoare logic to systems described by network diagrams that give a visual representation for sets of equations. An important motivating example is the notion of signal flow graph in control theory which forms the basis of systems such as Simulink. Network diagrams of this sort can be given several different formal semantics, e.g., in terms of differential equations or in terms of recurrence relations.

To apply our approach to network diagrams, our starting point is again the category of sets and relations, but now using the monoidal structure given by the cartesian product. The *cartesian trace*,  $\text{Tr}(r)$ , of a relation  $r : X \times Z \rightarrow Y \times Z$  is defined by:

$$x \underline{\text{Tr}}(r) y \equiv \exists z(\langle x, z \rangle \underline{r} \langle y, z \rangle).$$

In a network diagram,  $r$  above will be thought of as the input-output relation of a subsystem, with  $z$  representing a control signal that is fed back from output to input to form the overall system. The input-output relation of the overall system is then the relation between  $X$  and  $Y$  determined by solving the system of equations denoted by  $r$  for the value of the control signal,  $z$ .

Algebraic structure will be important to us. Fortunately, the cartesian trace gives rise to a wide range of interesting TMCs with nice algebraic properties. In the sequel, we will first show that the cartesian trace applies to many interesting categories of relations enjoying useful algebraic or other structure. We then present a diagrammatic language for stream circuits and give it a semantics in terms of an algebraic TMC  $S_{\text{SC}}$  of relations representing a formal abstraction of the differential equation semantics. We use a syntactic criterion to identify a useful class of “valid” circuits, whose semantic values are total functions. The subcategory formed by these circuits admits a verification functor and so this gives a sound and complete Hoare logic for the valid circuits.

### 5.1 Traced Monoidal Categories of Relations

Let  $C$  be any concrete category, so that each object  $X$  of  $C$  has an associated *underlying set* (also written  $X$  by abuse of notation) and each  $C$ -morphism between  $C$ -objects  $X$  and  $Y$  is a function from  $X$  to  $Y$ , morphisms being composed via functional composition. Let us assume that (i) set-theoretic finite products and equalizers<sup>5</sup> also serve as finite products and equalizers in  $C$ , and (ii), if  $f$  is a  $C$ -morphism then the graph of  $f$ , as a subset of  $X \times Y$ , is the range of some  $C$ -morphism, i.e., there is a  $C$ -object  $U$  and a  $C$ -morphism  $u : U \rightarrow X \times Y$ , with  $\text{Graph}(f) = \text{ran}(u)$ . These assumptions hold, for example, for any concrete category defined by a set of operations and equational laws, such as any of the usual algebraic categories: groups, vector spaces over a field, modules over a ring etc. In fact, for these categories, the converse of assumption (ii) holds: a function is a  $C$ -morphism iff its graph is the range of a  $C$ -morphism. Under these assumptions, define a  $C$ -relation between  $C$ -objects  $X$  and  $Y$  to be any relation,  $r$ , which is given, as a subset of  $X \times Y$ , by the range of a  $C$ -morphism, i.e.,  $r = \text{ran}(u)$ , for some  $C$ -object  $U$  and  $C$ -morphism  $u : U \rightarrow X \times Y$ . We then define the category  $\text{Rel}(C)$  to have the same objects as  $C$  and to have as morphisms between  $X$  and  $Y$  the set of all  $C$ -relations between  $X$  and  $Y$ , morphisms being composed by relational composition.

**THEOREM 5.1.** *Under the above assumptions on the concrete category  $C$ ,  $\text{Rel}(C)$  together with cartesian product and cartesian trace forms a TMC having  $C$  as a subcategory. If, moreover, a function is a  $C$ -morphism iff its graph is the range of some  $C$ -morphism, then this subcategory comprises precisely those relations in  $\text{Rel}(C)$  which are set-theoretic functions.*

**Proof.** We must first verify that  $\text{Rel}(C)$  actually is a category: by assumption (i) if  $X$  is any  $C$ -object, then the diagonal function  $\delta = x \mapsto \langle x, x \rangle$  is a  $C$ -morphism

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<sup>5</sup>Recall that in any category, an equalizer for two morphisms  $f, g : X \rightarrow Y$  is a universal arrow  $h : E \rightarrow X$  making the horizontal composites in the diagram  $E \xrightarrow{h} X \xrightarrow{f, g} Y$  equal. In the category of sets, an equalizer of any two maps  $f, g : X \rightarrow Y$  is given by the inclusion of the subset  $\{x : X \mid f(x) = g(x)\}$ , which we will call the equalizer of  $f$  and  $g$ .

from  $X$  to  $X \times X$  and  $\text{ran}(\delta)$  is the identity relation on  $X$  which is therefore a  $\text{Rel}(C)$ -morphism acting as a two-sided identity for relational composition. Given  $\text{Rel}(C)$ -morphisms  $r : X \rightarrow Y$  and  $s : Y \rightarrow Z$ , there are  $C$ -objects  $U$  and  $V$  and  $C$ -morphisms  $u : U \rightarrow X \times Y$  and  $v : V \rightarrow Y \times Z$  with  $r = \text{ran}(u)$  and  $s = \text{ran}(v)$ . Let us write  $\pi_i : X_1 \times X_2 \rightarrow X_i$  for the projections of a binary product onto its factors and write  $\pi_{ij}$  for the composite  $(\pi_i; \pi_j)$ . Our data provide everything except the object  $E$  and the dotted morphisms in the following diagram.

$$\begin{array}{ccccc}
 & i & & & \\
 E & \xrightarrow{\quad \dots \quad} & U \times V & \xrightarrow{u \times v} & (X \times Y) \times (Y \times Z) \xrightarrow{\pi_{12}} Y \\
 & e & \searrow & & \downarrow \pi_1 \times \pi_2 \\
 & & & & X \times Z
 \end{array}$$

If we take  $i : E \rightarrow U$  to be the equalizer of the two horizontal composites from  $U \times V$  to  $Y$  and let  $e : E \rightarrow X \times Z$  be the composite  $(i; u \times v; \pi_1 \times \pi_2)$  then  $e$  is a  $C$ -morphism and  $\text{ran}(e) = (r; s)$ . Thus  $\text{Rel}(C)$  has identities and is closed under relational composition and hence forms a category. It is easy to verify that the cartesian product makes  $\text{Rel}(C)$  into a symmetric monoidal category and, using assumption (ii), that  $C$  forms a subcategory, and that this subcategory coincides with the functional relations in  $\text{Rel}(C)$  under the stated condition. So it remains to show  $\text{Rel}(C)$  is closed under cartesian trace. So assume  $r : X \times Z \rightarrow Y \times Z$  is a  $\text{Rel}(C)$ -morphism, so that there is a  $C$ -object  $U$  and a  $C$ -morphism  $u : U \rightarrow (X \times Z) \times (Y \times Z)$  with  $r = \text{ran}(u)$ . This gives everything except the object  $E$  and the dotted morphisms in the following diagram.

$$\begin{array}{ccccc}
 & i & & & \\
 E & \xrightarrow{\quad \dots \quad} & U & \xrightarrow{u} & (X \times Z) \times (Y \times Z) \xrightarrow{\pi_{12}} Z \\
 & e & \searrow & & \downarrow \pi_1 \times \pi_1 \\
 & & & & X \times Y
 \end{array}$$

If we take  $i : E \rightarrow U$  to be the equalizer of the two horizontal composites from  $U$  to  $Z$  and let  $e : E \rightarrow X \times Y$  be the composite  $(i; u; \pi_1 \times \pi_1)$  then  $\text{Tr}(r) = \text{ran}(e)$  so the cartesian trace is a  $\text{Rel}(C)$ -morphism and the proof is complete.  $\square$

As an aside, we note that this theorem offers an alternative to the partial traced monoidal categories that have been considered by several researchers, e.g., see [Haghverdi and Scott 2005]. Rather than a partial trace on  $C$ , it gives a total trace on  $\text{Rel}(C)$  which contains  $C$  as a subcategory. Specific examples of partial TMCs given by Haghverdi and Scott [2005] are the category of vector spaces and the category of complete metric spaces with non-expansive continuous functions as morphisms. Both these examples are covered by the above theorem and without the restriction to finite dimensions in the case of vector spaces. The theorem also applies to both Hilbert spaces and Banach spaces with bounded operators as morphisms and to compact Hausdorff topological spaces with continuous maps.

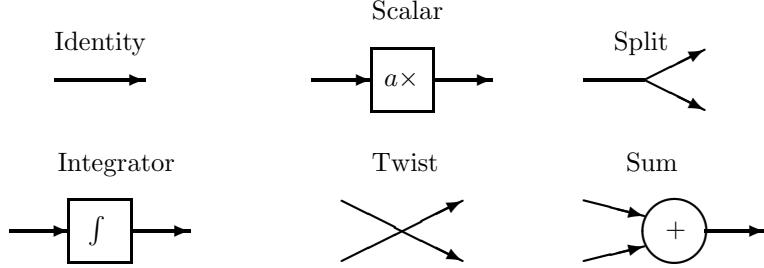


Fig. 13. Basic stream circuits.

Perhaps surprisingly, the dual of the construction in the above theorem also goes through using coequalizers, coproducts and an appropriate notion of cograph. Constructions of this sort have been considered in connection with universal algebra, see [Hutchinson 1994] for details and references.

### 5.2 The programming language: stream circuits

In [Boulton et al. 2003], a Hoare logic was presented for the frequency analysis of linear control systems with feedback, modelled as linear differential equations, an approach which we generalised and systematised in [Arthan et al. 2007]. In the present paper, we will model linear differential equations as stream circuits (see [Rutten 2004]). With Theorem 5.1 in mind, this model fits nicely both with the algebraic viewpoint of [Arthan et al. 2007] and with the present framework.

Our language of stream circuits comprises finite diagrams, made up from the basic circuits shown in Figure 13 (where  $a$  is a real number-valued parameter). New circuits are formed from old by connecting outputs (arrow heads) to inputs (arrow tails). An output of a circuit may be connected to an input of the same circuit, so we can create feedback loops. Following [Rutten 2004], we say circuit is *valid* if every closed path in the circuit passes through at least one integrator (or “register” in Rutten’s terminology).

### 5.3 The semantics

In our semantics, stream circuits denote differential equations. As demonstrated in [Escardó and Pavlović 1998], many computational aspects of mathematical analysis can be developed in a co-algebraic setting using infinite streams of real numbers, an analytic function  $f$  being modelled by the infinite stream  $[f(0), f'(0), f''(0), \dots]$  of its partial derivatives. For many purposes we may abstract away issues of convergence, and view infinite streams rather than functions as the main objects of interest. Following [Rutten 2004], we can then view a stream circuit as just a system of equations on streams: for example, the differential equation  $f = f'$ , or equivalently  $f = (\int f) + c$ , corresponds to a stream equation  $f = c :: \text{tl}(f)$ .

To turn these ideas into a formal semantics for stream circuits, let  $\Sigma$  denote the set of streams of real numbers, i.e. all functions  $\mathbb{N} \rightarrow \mathbb{R}$ . Let the objects of  $\mathcal{S}_{\text{SC}}$  be the set containing the singleton set  $\{\varepsilon\}$  and  $\Sigma$ , and closed under cartesian product, i.e. the objects are  $\Sigma^0 = \{\varepsilon\}, \Sigma^1 = \Sigma, \Sigma^2 = \Sigma \times \Sigma, \dots, \Sigma^m, \dots$ . Consider the following basic morphisms between the objects of  $\mathcal{S}_{\text{SC}}$ :

|   |   |
|---|---|
| $\text{—Wire, } \text{id} : \Sigma \rightarrow \Sigma$      | $\text{—Copy, } \text{c} : \Sigma \rightarrow \Sigma \times \Sigma$                         |
| $\text{id}(\sigma) := \sigma$                               | $\text{c}(\sigma) := \langle \sigma, \sigma \rangle$  |
| $\text{—Scalars, } (a \times) : \Sigma \rightarrow \Sigma$  | $\text{—Sum, } (+) : \Sigma \times \Sigma \rightarrow \Sigma$                               |
| $(a \times)(\sigma) := [a\sigma_0, a\sigma_1, \dots]$       | $(+)\langle \sigma, \sigma' \rangle := [\sigma_0 + \sigma'_0, \sigma_1 + \sigma'_1, \dots]$ |
| $\text{—Integrator, } \text{R} : \Sigma \rightarrow \Sigma$ | $\text{—Twist, } \text{t} : \Sigma \times \Sigma \rightarrow \Sigma \times \Sigma$          |
| $\text{R}(\sigma) := [0, \sigma_0, \sigma_1, \dots]$        | $\text{t}(\sigma, \sigma') := \langle \sigma', \sigma \rangle$                              |

The morphisms of  $\mathcal{S}_{\text{SC}}$  are the *relations* obtained from the basic morphisms above viewed as relations (via their graph) and closing this set under relational composition, cartesian product of relations, and the trace for the cartesian product.

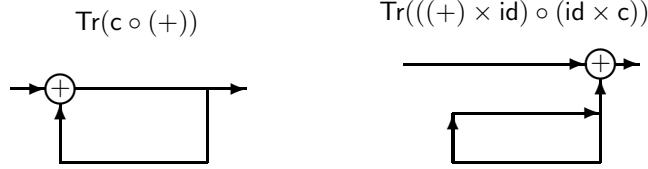


Fig. 14. Stream circuits not defining functions

The diagrams in Figure 14 illustrate two stream circuits that define relations that are not total functions. In the circuit on the left there is no fixed point for the input stream  $\sigma_a := [a, a, \dots]$  unless  $a = 0$ , so the circuit defines a partial function with domain  $\{0\}$ . On the other hand, in the circuit on the right any stream  $\tau$  is a fixed point of the trace, so that any input stream  $\sigma$  is related to any output stream, so the circuit represents the “chaotic” relation  $\Sigma \times \Sigma$ . The notion of valid circuit gives a syntactic criterion for avoiding this pathological behaviour: while  $\mathcal{S}_{\text{SC}}$  is a category of relations that are partial and one-to-many in general, valid circuits denote *total* functions, i.e. a valid circuit denotes a system of equations whose solutions always exist and are unique (as we shall prove shortly).

It is again easy to see that  $\mathcal{S}_{\text{SC}}$  forms a symmetric monoidal category, with the monoidal structure of cartesian product. Finally, with the standard family of trace relations (defined in Section 2),  $\mathcal{S}_{\text{SC}}$  forms a traced symmetric monoidal category.  $\mathcal{S}_{\text{SC}}$  has several important subcategories obtained by restricting the sets of morphisms:  $\mathcal{S}_{\text{SC}}^F$  in which the morphisms are the total functional relations, and  $\mathcal{S}_{\text{SC}}^V$  comprising the denotations of valid stream circuits. To understand these subcategories it is helpful to consider some additional algebraic structure. Let  $\mathbf{R} = \mathbb{R}[[x]]$  be the ring of formal power series with real coefficients, e.g., see Mac Lane and Birkhoff [1999], section VIII.9. Thus elements of  $\mathbf{R}$  are formal expressions  $f_0 + f_1x + f_2x^2 + \dots + f_mx^m + \dots$  where the coefficients  $f_i$  are real numbers. Note that a univariate polynomial,  $p_0 + p_1x + p_2x^2 + \dots + p_mx^m$ , over  $\mathbb{R}$  may be viewed as a formal power series in which all but a finite number of the coefficients are zero. Formal power series are added by adding corresponding coefficients and multiplied using the convolution product (generalising multiplication of polynomials), i.e., if  $f = f_0 + f_1x + \dots + f_mx^m + \dots$  and  $g = g_0 + g_1x + \dots + g_mx^m \dots$ , the coefficients

$$\text{fdbck}(f) := \text{Tr}((\text{c}) \circ (+) \circ (\text{id} \uplus f)) \quad \text{sum}(f, g) := (+) \circ (g \uplus f) \circ (\text{c})$$



Fig. 15. Feedback and summation in  $\mathcal{S}_{\text{SC}}$

of the sum  $f + g$  and the product  $fg$  are given by:

$$\begin{aligned} (f + g)_m &= f_m + g_m \\ (fg)_m &= f_0 g_m + f_1 g_{m-1} + \dots + f_{m-1} g_1 + f_m g_0 \end{aligned}$$

Under these operations  $\mathbf{R}$  forms a commutative ring with  $0 = 0 + 0x + \dots$  and  $1 = 1 + 0x + \dots$ . If we identify a stream  $\sigma$  with the formal power series  $\sigma_0 + \sigma_1 x^1 + \dots + \sigma_m x^m + \dots$ , then  $\mathbf{R}$  acts on the objects of  $\mathcal{S}_{\text{SC}}$  by multiplication, i.e., for  $(^1\sigma, \dots, ^n\sigma) \in \Sigma^n$  and  $f \in \mathbf{R}$ , we define:

$$(^1\sigma, \dots, ^n\sigma)f = (^1\sigma f, \dots, ^n\sigma f)$$

Under this action, the objects of  $\mathcal{S}_{\text{SC}}$  become  $\mathbf{R}$ -modules<sup>6</sup>.

If  $f \in \mathbf{R}$  and  $f_0 \neq 0$ , then  $f$  has a multiplicative inverse  $f^{-1}$  whose coefficients are given recursively by

$$\begin{aligned} (f^{-1})_0 &= 1/f_0 \\ (f^{-1})_{m+1} &= (f_1(f^{-1})_m + f_2(f^{-1})_{m-1} + \dots + f_{m+1}(f^{-1})_0)/f_0 \end{aligned}$$

as is readily checked. Clearly, any non-zero  $f \in \mathbf{R}$  may be written uniquely in the form  $f = x^m g$  where  $g$  is invertible,  $m$  being the *order* of  $f$ , i.e., the smallest  $m$  such that  $f_m \neq 0$ . It follows that the non-zero ideals of  $\mathbf{R}$  are precisely the principal ideals generated by the powers of  $x$ , i.e.,  $I \neq \{0\}$  is an ideal of  $\mathbf{R}$  iff  $I = x^m \mathbf{R}$  for some  $m$ .

It is easy to check that the basic morphisms of  $\mathcal{S}_{\text{SC}}$  are  $\mathbf{R}$ -module homomorphisms, so for example, if  $\sigma \in \Sigma^n$  and  $f \in \mathbf{R}$ , one has  $\text{c}(\sigma f) = \text{c}(\sigma)f$ . Noting that the approach of [Arthan et al. 2007] generalises to  $\mathbf{R}$ -modules<sup>7</sup>, we find that each morphism of  $\mathcal{S}_{\text{SC}}$  is actually an additive relation, i.e., each morphism between objects  $L$  and  $M$  of  $\mathcal{S}_{\text{SC}}$  is a relation whose graph is a submodule of the product module

<sup>6</sup>Recall that the notion of  $R$ -module is the generalisation to arbitrary rings of the notion of vector space over a field: an  $R$ -module is an additive group  $M$  such that  $R$  acts on  $M$  as a ring of linear operators. I.e., writing  $x \mapsto xf$  for the action of  $f \in R$  on  $x \in M$ , one has  $(x+y)f = xf + yf$ ,  $x1 = x$  and  $x(fg) = (xf)g$ .  $R$  acts itself by right multiplication and so becomes an  $R$ -module whose submodules are called *ideals*, i.e., an ideal  $I$  is an additive subgroup of  $R$  such that, for any  $a \in R$ ,  $Ia = \{xa \mid x \in I\} \subseteq I$ . See, e.g., [Mac Lane and Birkhoff 1999] chapter V for the basics of module theory.

<sup>7</sup>The only difficulty is with theorem 5.8 of [Arthan et al. 2007] where the proof in the published paper uses properties of bases to define relational inverse in terms of the feedback loop operator. However, a basis-free construction can be given: in the notation of [Arthan et al. 2007], if  $r : V \leftrightarrow$

$L \times M$ . Within  $\mathcal{S}_{\text{SC}}$ ,  $\mathcal{S}_{\text{SC}}^T$  comprises the subcategory of morphisms which happen to be total functions. In the terminology of Section 5.1, the methods of [Arthan et al. 2007, section 5] show that  $\mathcal{S}_{\text{SC}}$  is the category of  $\mathcal{S}_{\text{SC}}^T$ -relations:  $\mathcal{S}_{\text{SC}} = \text{Rel}(\mathcal{S}_{\text{SC}}^T)$

Let us exploit this algebraic structure to investigate the cartesian trace applied to a functional morphism in  $\mathcal{S}_{\text{SC}}$ . Using the vanishing law for the trace, the trace of a general morphism  $\Sigma^m \times \Sigma^k \rightarrow \Sigma^n \times \Sigma^k$  can be calculated by iterating the trace on morphisms  $\Sigma^{m'} \times \Sigma \rightarrow \Sigma^{n'} \times \Sigma$ , so it suffices to consider the case  $k = 1$ . So let  $F : \Sigma^m \times \Sigma \rightarrow \Sigma^n \times \Sigma$  be a functional morphism, i.e., a homomorphism of  $\mathbf{R}$ -modules. Just as with vector spaces, we can represent  $F$  as a  $2 \times 2$  block matrix, i.e., there are unique  $\mathbf{R}$ -module homomorphisms,  $A : \Sigma^m \rightarrow \Sigma^n$ ,  $B : \Sigma \rightarrow \Sigma^n$ ,  $C : \Sigma^m \rightarrow \Sigma$  and  $D : \Sigma \rightarrow \Sigma$ , such that  $\langle \alpha, \sigma \rangle F = \langle \beta, \tau \rangle$  where:

$$\begin{aligned}\beta &= \alpha A + \sigma B \\ \tau &= \alpha C + \sigma D\end{aligned}$$

$D$  here is a  $1 \times 1$  matrix, i.e., under the identification of  $\Sigma$  with  $\mathbf{R}$ ,  $D$  is given by multiplication by some element of  $\mathbf{R}$  say  $D = (d)$ . The following lemma says that under appropriate assumptions on  $d$ , the trace of  $F$  is a total function. The ideal  $I$  in the theorem may be thought of as a measure of the precision of an approximation  $\sigma$  to the actual fixed point  $\sigma'$ . For the simple existence of the fixed point, take  $I = \mathbf{R}$  and pick an arbitrary  $\sigma$ .

LEMMA 5.2. *Assume  $F : \Sigma^m \times \Sigma \rightarrow \Sigma^n \times \Sigma$  is defined by  $\langle \alpha, \sigma \rangle F = \langle \beta, \tau \rangle$  where:*

$$\begin{aligned}\beta &= \alpha A + \sigma B \\ \tau &= \alpha C + \sigma D\end{aligned}$$

*for some  $A : \Sigma^m \rightarrow \Sigma^n$ ,  $B : \Sigma \rightarrow \Sigma^n$ ,  $C : \Sigma^m \rightarrow \Sigma$  and  $D : \Sigma \rightarrow \Sigma$ , where  $D$  is given by the  $1 \times 1$  matrix  $(d)$  where  $d \in \mathbf{R}$  is either 0 or has positive order, i.e.,  $d = xf$  for some  $f \in \mathbf{R}$ . Let  $I$  be any ideal in  $\mathbf{R}$  and assume  $\langle \alpha, \sigma \rangle F = \langle \beta, \tau \rangle$  for some  $\alpha, \beta, \sigma$  and  $\tau$  where  $\sigma - \tau \in I$ , then there exist unique  $\beta'$  and  $\sigma'$  such that  $\langle \alpha, \sigma' \rangle F = \langle \beta', \sigma' \rangle$  and then one has  $\sigma' - \sigma \in I$ .*

**Proof.** For uniqueness, if  $\beta'$  and  $\sigma'$  satisfy

$$\begin{aligned}\beta' &= \alpha A + \sigma' B \\ \sigma' &= \alpha C + \sigma' D\end{aligned}$$

then, as  $D = (d)$  and  $1 - d$  is invertible, the second equation gives  $\sigma' = \alpha C / (1 - d)$  fixing  $\sigma'$  and then the first equation fixes  $\beta'$ . For the existence, if  $I = \{0\}$ , then  $\tau = \sigma$  and we may take  $\beta' = \beta$  and  $\sigma' = \sigma$ , otherwise define sequences  ${}^m\sigma$  and  ${}^m\tau$  of elements of  $\mathbf{R}$  as follows:

$$\begin{aligned}{}^0\sigma &= \sigma, & {}^0\tau &= \tau = \alpha C + {}^0\sigma d \\ {}^{m+1}\sigma &= {}^m\tau, & {}^{m+1}\tau &= \alpha C + {}^{m+1}\sigma d\end{aligned}$$

$W, r^{-1}$  is the composite:

$$\begin{aligned}(0, 1_W) &: W \rightarrow V \times W; \\ \text{loop}(1_{V \times W}, 1_{V \times W} - (\pi_1; r; (0, 1_W))) &: V \times W \leftrightarrow V \times W; \\ \pi_1 &: V \times W \rightarrow V\end{aligned}$$

We claim that  ${}^m\sigma - {}^m\tau \in Id^m$  for each  $m$ . This is true by assumption for  $m = 0$ , so assume inductively that it holds for some  $m$ , we then have

$$\begin{aligned} {}^{m+1}\sigma - {}^{m+1}\tau &= {}^m\tau - (\alpha C + {}^m\tau d) \\ &= {}^m\tau(1 - d) - \alpha C \\ &= {}^m\tau(1 - d) - {}^m\tau + {}^m\sigma d \\ &= ({}^m\sigma - {}^m\tau)d \in (Id^m)d = Id^{m+1} \end{aligned}$$

which proves the claim. Note that as  $d = xf$  for some non-zero  $f$ ,  ${}^m\sigma_k = {}^{m+1}\sigma_k$  for  $m > k$  since we have  ${}^{m+1}\sigma - {}^m\sigma = {}^m\tau - {}^m\sigma \in Id^m = If^m x^m$ . It follows that if we put  $\sigma'_k = {}^{k+1}\sigma_k$ , then  $\sigma' - {}^m\sigma \in Id^m$  for all  $m$  and if we put  $\tau' = \alpha C + \sigma'D$  then  $\sigma' - \tau' \in Id^m$  for all  $m$ , but the intersection of the ideals  $Id^m$  is the zero ideal, so  $\sigma' - \tau' = 0$ , i.e.,  $\sigma' = \tau' = \alpha C + \sigma'D$ ; taking  $\beta' = \alpha A + \sigma'B$  completes the proof.

□

Remark: the solution in the case where  $d = 0$ , can also be given directly as  $\sigma' = \alpha C$  and  $\beta' = \alpha(A + CB)$ .

**THEOREM 5.3.** *Valid stream circuits denote total functions.*

By the lemma, it suffices to show that if a circuit denotes a function  $F : \Sigma^m \times \Sigma \rightarrow \Sigma^n \times \Sigma$  given by  $\langle \alpha, \sigma \rangle F = \langle \beta, \tau \rangle$  where:

$$\begin{aligned} \beta &= \alpha A + \sigma B \\ \tau &= \alpha C + \sigma D \end{aligned}$$

$D = (d)$  being a  $1 \times 1$  matrix, and if every path in the circuit from the second input component to the second output component passes through an integrator, then  $d = xf$  for some  $f \in R$ . This is easy to verify by induction on the construction of the circuit, since passing a stream through an integrator corresponds to multiplying it by  $x$ . □

#### 5.4 Hoare logic for stream circuits

We will now construct a functor  $H_{\mathcal{SC}} : \mathcal{S}_{\mathcal{SC}} \rightarrow \mathcal{H}$  and show that its restriction to valid circuits is a verification functor. Each object of  $\mathcal{S}_{\mathcal{SC}}$  has the form  $\Sigma^m = \Sigma \times \dots \times \Sigma$  for some  $m$ . We define  $H_{\mathcal{SC}}(\Sigma)$  to be the set of pairs  $(\sigma, I)$  where  $\sigma \in \Sigma$  and  $I \subseteq \Sigma$  corresponds to an ideal in  $R$  under the identification of  $\Sigma$  with  $R$ .  $H_{\mathcal{SC}}(\Sigma)$  is ordered by defining  $\langle \sigma, I \rangle \sqsubseteq \langle \tau, J \rangle$  iff  $\sigma + I \subseteq \tau + J$ .  $H_{\mathcal{SC}}(\Sigma^m)$  is then defined to be  $H_{\mathcal{SC}}(\Sigma)^m$  which we may identify with the set of all pairs  $\langle \alpha, L \rangle$  where  $\alpha \in \Sigma^m$  and  $L$  is what we may call a *coordinate submodule* of  $\Sigma^m$ , i.e., a submodule of the form  $I_1 \times I_2 \times \dots \times I_m$  where each  $I_k$  is an ideal of  $R$  under the identification of  $\Sigma$  with  $R$ . Note that the  $m$ -tuple  $\langle I_1, I_2, \dots, I_m \rangle$  can be recovered from the product  $I_1 \times I_2 \times \dots \times I_m$  since the ideals  $I_j$  are non-empty sets. The product ordering on  $H_{\mathcal{SC}}(\Sigma)^m$  corresponds to taking  $\langle \alpha, L \rangle \sqsubseteq \langle \beta, M \rangle$  iff  $\alpha + L \subseteq \beta + M$ . Thus we let  $\langle \alpha, L \rangle$  correspond to the subset  $\alpha + L$  of  $\Sigma^m$  and think of  $\alpha$  as an estimate of a value in  $\Sigma^m$  with  $L$  giving the precision of the estimate. So for example,  $\langle \alpha, \Sigma^m \rangle$  represents the whole set, or a maximally imprecise estimate, while  $\langle \alpha, 0 \rangle$  represents the singleton set  $\{\alpha\}$ , or an exact estimate.

On the morphisms (semantic values of stream circuits)  $f : X \rightarrow Y$  in  $\mathcal{S}_{\text{SC}}$ , we define the relation  $H_{\text{SC}}(f)$  as follows, where  $\langle \alpha, L \rangle \in H_{\text{SC}}(X)$  and  $\langle \beta, M \rangle \in H_{\text{SC}}(Y)$ , and where  $(A)r$  denotes the image of the set  $A$  under the relation  $r$ .

$$\langle \alpha, L \rangle \underline{H_{\text{SC}}(f)} \langle \beta, M \rangle : \equiv \alpha + L \subseteq \text{dom}(f) \wedge (\alpha + L)f \subseteq \beta + M$$

We now show that  $H_{\text{SC}}$  as defined above yields a verification functor for valid circuits.

**THEOREM 5.4.**  $H_{\text{SC}} : \mathcal{S}_{\text{SC}}^V \rightarrow \mathcal{H}$ , as defined above, is a verification functor for valid circuits.

**Proof.** That  $H_{\text{SC}}$  commutes with composition of morphisms is easy to verify, so  $H_{\text{SC}}$  is indeed a functor. By construction,  $H_{\text{SC}}(X \times Y) = H_{\text{SC}}(X) \times H_{\text{SC}}(Y)$  (although in our notation we are identifying  $(\Sigma \times \mathbb{P}\Sigma)^m$  with a subset of  $(\Sigma^m \times \mathbb{P}(\Sigma^m))$ ), and one may check that  $H_{\text{SC}}(f \times g) = H_{\text{SC}}(f) \times H_{\text{SC}}(g)$  when  $f$  and  $g$  are morphisms, so  $H_{\text{SC}}$  is a monoidal functor. It remains to show that  $H_{\text{SC}}$  commutes with the trace. As in Theorem 5.3, it suffices to consider traces of morphisms  $f : \Sigma^m \times \Sigma \rightarrow \Sigma^n \times \Sigma$ . So assume  $\langle \alpha, L \rangle \underline{H_{\text{SC}}(\text{Tr}(f))} \langle \beta, M \rangle$ , then by the definition of  $H_{\text{SC}}$ ,  $\alpha + L \subseteq \text{dom}(\text{Tr}(f))$  and so in particular,  $\alpha \in \text{dom}(\text{Tr}(f))$  which means that there is  $\beta' \in \Sigma^n$  and  $\sigma \in \Sigma$ , such that  $\langle \alpha, \sigma \rangle f \langle \beta', \sigma \rangle$ . But then from the definition of  $H_{\text{SC}}$  we must have that  $\beta' - \beta \in M$  and that

$$\langle \langle \alpha, \sigma \rangle, L \times 0 \rangle \underline{H_{\text{SC}}(f)} \langle \langle \beta, \sigma \rangle, M \times 0 \rangle$$

which means that  $H_{\text{SC}}(\text{Tr}(f)) \subseteq \text{Tr}(H_{\text{SC}}(f))$ . Conversely, assume that

$$\langle \alpha, L \rangle \underline{\text{Tr}(H_{\text{SC}}(f))} \langle \beta, M \rangle,$$

so that there is a stream  $\sigma$  and an ideal  $I$  such that

$$\langle \langle \langle \alpha, \sigma \rangle, L \times I \rangle \underline{H_{\text{SC}}(f)} \langle \langle \beta, \sigma \rangle, M \times I \rangle.$$

Then  $\langle \alpha, \sigma \rangle \in \text{dom}(f)$ , so that there are  $\gamma$  and  $\tau$  such that  $\langle \alpha, \sigma \rangle f = \langle \gamma, \tau \rangle$ , where  $\gamma - \beta \in M$  and  $\sigma - \tau \in I$ . Now, just as in Theorem 5.3 the assumption that the circuit is valid lets us apply Lemma 5.2 to give unique  $\delta$  and  $\sigma'$  such that  $\langle \alpha, \sigma' \rangle f = \langle \delta, \sigma' \rangle$  such that  $\delta - \gamma \in M$  and  $\sigma' - \sigma \in I$ , but then also  $\delta - \beta \in M$  and we may check that  $\langle \alpha, L \rangle \underline{H_{\text{SC}}(\text{Tr}(f))} \langle \beta, M \rangle$ . Thus  $\text{Tr}(H_{\text{SC}}(f)) \subseteq H_{\text{SC}}(\text{Tr}(f))$  completing the proof.  $\square$

This gives rise to a sound and complete Hoare-logic system for reasoning about *valid* stream circuits. Notice that the rules are only sound for valid circuits. For instance, consider the circuit  $c \circ (+) : \Sigma \rightarrow \Sigma \times \Sigma$  used to construct the example shown on the left of Figure 14. For any  $\alpha$ , the triple  $\{\langle \alpha, 0 \rangle, \Sigma \times \Sigma\} c \circ (+) \{0, \Sigma \times \Sigma\}$  is valid, but the triple  $\{\langle \alpha, \Sigma \rangle\} \text{Tr}(c \circ (+)) \{\Sigma\}$  is only valid for  $\alpha = 0$ .

Streams can be viewed as giving the Taylor expansions of analytic functions. in this particular example, the pre- and post-conditions in the Hoare logic correspond to partial sums for the Taylor expansions. A partial sum comprising  $n$  terms corresponds to the specification  $\alpha + \Sigma x^n$ , where  $\alpha$  is any stream with  $\alpha_i = s_i$  for  $i = 0, \dots, n - 1$ . We have thus obtained from our general categorical construction a sound and complete formal system  $\mathbf{HL}(\mathcal{L}, \llbracket \cdot \rrbracket, H_{\text{SC}})$ , for reasoning about valid

Fig. 16. Derivation of feedback rule in  $\mathbf{HL}(\mathcal{L}, [\cdot], H)$

stream circuits and their input-output behaviour over classes of functions with a common partial Taylor sum.

If the simple feedback circuit  $\text{fdbck}(f)$  is defined in  $\mathcal{S}_{\text{SC}}$  by  $\text{Tr}((+); f; (c))$ , we then have the following rule, whose derivation is given in Figure 16 and is very similar to the derivation of the rule for while loops (cf. Figure 10).

$$\frac{\{s + t\} f \{t\}}{\{s\} \text{fdback}(f) \{t\}}$$

## 5.5 Hilbert Spaces

We have already remarked that the constructions of Section 5.1 apply to the category of Hilbert spaces and bounded operators. This gives a TMC whose objects are Hilbert spaces and whose morphisms are relations whose graphs are complete subspaces of a product space. Restricting to a specific Hilbert space  $\Sigma$  and its finite products  $\Sigma^m$ , analogues of all our results on stream circuits go through using closed discs,  $I_t = \{\sigma : \Sigma \mid \|\sigma\| \leq t\}$  in place of ideals of  $\mathbb{R}$ . The analogue of 5.2 requires  $D$  to be a contraction mapping ( $\|D\| = \sup_\sigma (\|\sigma D\| / \|D\|) < 1$ ) and then the fixed point can be given as an analytic limit. For the lemma to serve its purpose, the hypothesis on the approximate fixed point needs to be slightly stronger than for stream circuits, but the assumptions available where the lemma is used in Theorem 5.4 are sufficient.

The Hoare logic obtained in this way for systems where the semantic domains are Hilbert spaces way is interesting to compare with the Hoare logic for vector space semantics in general given in [Arthan et al. 2007]. In the general approach we do indeed get a very general Hoare logic but at the price of rules involving side-conditions that have a non-trivial semantic content. The approach for Hilbert spaces sketched here admits a much less general form of assertion and applies to a restricted category of spaces but those restrictions mean that it falls within the general framework of the present paper and yields rules that are much more satisfactory from a syntactic point of view.

## 6. CONCLUSION AND RELATED WORK

Several abstractions of the Floyd-Hoare logic can be found in the literature. In this final section we attempt to clarify the relation between the work presented here, and the previous work. Due to the vast amount of research in the area, we will

only cover, however, works that we believe are closely related to ours.

Kozen's [2000] *Kleene Algebra with Test*, KAT, consists essentially of a Kleene algebra with a Boolean subalgebra. In Kozen's work Hoare triples  $\{P\} A \{Q\}$  are modelled as equations  $PA = PAQ$ , using the multiplication available in KAT. The rules of Hoare logic are then obtained as consequences of the equational theory of KAT. Although our work is based on similar ideas (reducing Hoare triples to pre-order statements) there does not seem to be at the moment a clear cut connection between the two approaches. Whereas Kozen relies on the rich theory of KAT to derive the usual rules of Hoare logic, in our development we use a minimal theory of pre-ordered sets for obtaining soundness and completeness.

Kozen's work is related to previous work on iteration theory [Bloom and Ésik 1991; Manes and Arbib 1986] and dynamic logic [Pratt 1976]. It should be stressed that all these focus on the semantics of Hoare logic over flowcharts and while programs, where the intrinsic monoidal structure is *disjoint union*. As we have shown in Section 5, our approach is more general including systems with an underlying *cartesian structure* as well.

Abramsky et al. [1996] have also studied the categorical structure of Hoare logic, using the notion of *specification structures*. It is easy to see that a TMC  $\mathcal{S}$  together with a verification functor  $H : \mathcal{S} \rightarrow \mathcal{H}$  gives rise to a specification structure:  $H$  maps objects  $X \in \mathcal{S}_o$  to sets  $H(X)$ , and  $H(f)(P) \sqsubseteq Q$  defines a ternary relation  $H(X) \times \mathcal{S}(X, Y) \times H(Y)$ . The extra structure of pre-order and trace, however, allows us to prove an abstract completeness theorem, which does not seem to be the focus of [Abramsky et al. 1996].

Blass and Gurevich [2000] considered the *underlying logic of Hoare logic*. Since Cook's celebrated completeness result [Cook 1978], Cook-expressive first-order logics have been used in proofs of relative completeness for Hoare logic. Blass and Gurevich have shown that existential fixed-point logic **EFL** is sufficient for proving Cook's completeness result, without the need for Cook's expressiveness condition. **EFL** contains the necessary constructions to ensure that the functor  $H_{\text{FC}}(f)$  of Section 4.3 can be inductively built, rather than assumed to exist. The fixed-point construction is used in order to produce the fixed point  $Q$  of Lemma 4.1.

For a given semantic mapping  $\llbracket \cdot \rrbracket : \mathcal{L} \rightarrow \mathcal{S}$ , verification functor  $H : \mathcal{S} \rightarrow \mathcal{H}$  and program  $A : X \rightarrow Y$  in  $\mathcal{L}$ , the binary monotone relation  $\{\cdot\} A \{\cdot\}$ , i.e.

$$(\cdot) H(\llbracket A \rrbracket) (\cdot) : H(X) \times H(Y)$$

can be viewed as an order-preserving function  $H(X)^{\text{op}} \times H(Y) \rightarrow \mathbb{B}$ , where  $H(X)^{\text{op}}$  denotes the pre-order  $H(X)$  with the opposite ordering, and the usual ordering for the product and the booleans  $\mathbb{B}$  is assumed. If we generalise pre-orders to categories, and the booleans  $\mathbb{B}$  to an arbitrary category, the binary relation  $\{\cdot\} A \{\cdot\}$  becomes a profunctor (cf. [Cattani and Winskel 2004]). Such profunctors also arise from a generalisation of relation, so-called *span*, used by Winskel [2005] to represent processes. A span is a diagram of the form  $A \leftarrow U \rightarrow B$ , where  $A, B$  and  $U$  are event structures (partial orders endowed with a binary “consistency” relation). Such a diagram is equivalent to a morphism  $U \rightarrow A \times B$  as used in our definition of the category of  $C$ -relations. These observations suggest several possible links between our methods and Winskel's approach to non-determinism and concurrency.

## ACKNOWLEDGMENTS

We are grateful for discussions we have had with Martin Hyland, and his suggestion of using monotone relations (rather than functions) which considerably simplified our previous work [Martin et al. 2006]. We also thank Glynn Winskel for helpful correspondence and pointers to related work. We thank Colin O'Halloran of QinetiQ and John Hall of DSTL for a discussion which stimulated our interest in applying Hoare logics to systems expressed as network diagrams.

## REFERENCES

ABRAMSKY, S., GAY, S., AND NAGARAJAN, R. 1996. Specification structures and propositions-as-types for concurrency. In *Logics for Concurrency: Structure vs. Automata*, G. Birtwistle and F. Moller, Eds. *Logics for Concurrency: Structure vs. Automata*, 5–40.

APT, K. R. 1981. Ten years of Hoare's logic: A survey – Part 1. *ACM Transactions on Programming Languages and Systems* 3, 4 (October), 431–483.

ARTHAN, R., MARTIN, U., MATHIESEN, E. A., AND OLIVA, P. 2007. Reasoning about linear systems. In *SEFM'07*. IEEE Press.

BAINBRIDGE, E. S. 1976. Feedback and generalized logic. *Information and Control* 31, 75–96.

BERGER, M., HONDA, K., AND YOSHIDA, N. 2005. A logical analysis of aliasing in imperative higher-order functions. In *ICFP 2005*. 280–293.

BLASS, A. AND GUREVICH, Y. 2000. The underlying logic of Hoare logic. *Bull. of the Euro. Assoc. for Theoretical Computer Science* 70, 82–110.

BLOOM, S. L. AND ÉSIK, Z. 1991. Floyd-Hoare logic in iteration theories. *J. ACM* 38, 4 (October), 887–934.

BOULTON, R. J., HARDY, R., AND MARTIN, U. 2003. A Hoare logic for single-input single-output continuous time control systems. In *Proceedings of the 6th International Workshop on Hybrid Systems, Computation and Control*, Springer LNCS 2623, 113–125.

CATTANI, G. L. AND WINSKEL, G. 2004. Profunctors, open maps and bisimulation. Accepted for MSCS.

COOK, S. A. 1978. Soundness and completeness of an axiom system for program verification. *SIAM J. Comput.* 7, 1 (February), 70–90.

ESCARDÓ, M. H. AND PAVLOVIC, D. 1998. Calculus in coinductive form. In *Proceedings of the 13th Annual IEEE Symposium on Logic in Computer Science*.

FLOYD, R. W. 1967. Assigning meanings to programs. *Proc. Amer. Math. Soc. Symposia in Applied Mathematics* 19, 19–31.

HAGHVERDI, E. AND SCOTT, P. 2005. Towards a typed geometry of interaction. In *CSL'05, LNCS*, L. Ong, Ed. Vol. 3634. 216–231.

HOARE, C. A. R. 1969. An axiomatic basis for computer programming. *Communications of the ACM* 12, 10 (October), 576–585.

HUTCHINSON, G. 1994. Relation categories and coproduct congruence categories in universal algebra. *Algebra Universalis* 32, 609–647.

JOYAL, A., STREET, R., AND VERITY, D. 1996. Traced monoidal categories. *Mathematical Proceedings of the Cambridge Philosophical Society* 119, 447–468.

KOZEN, D. 2000. On Hoare logic and Kleene algebra with tests. *ACM Transactions on Computational Logic (TOCL)* 1, 1, 60–76.

MAC LANE, S. 1998. *Categories for the Working Mathematician*. Graduate texts in mathematics, vol. 5. Springer, 2nd ed.

MAC LANE, S. AND BIRKHOFF, G. 1999. *Algebra*, Third ed. AMS Chelsea Publishing.

MANES, E. G. AND ARBIB, M. A. 1986. *Algebraic Approaches to Program Semantics*. AKM series in theoretical computer science. Springer-Verlag, New York, NY.

MARTIN, U., MATHIESEN, E. A., AND OLIVA, P. 2006. Abstract Hoare logic. In *CSL'06, LNCS*.

O'HEARN, P., REYNOLDS, J., AND YANG, H. 2001. Local reasoning about programs that alter data structures. In *CSL'01, LNCS*, L. Fribourg, Ed. Vol. 2142. Springer-Verlag, 1–19.

PRATT, V. R. 1976. Semantical considerations on Floyd-Hoare logic. In *Proc. 17th Annual IEEE Symposium on Foundations of Computer Science*. 109–121.

REYNOLDS, J. C. 2002. Separation logic: A logic for shared mutable data structures. In *Proc. Logic in Computer Science (LICS'2002)*. 55–74.

RUTTEN, J. J. M. M. 2004. An application of stream calculus to signal flow graphs. *Lecture Notes in Computer Science* 3188, 276–291.

SIMPSON, A. K. AND PLOTKIN, G. D. 2000. Complete axioms for categorical fixed-point operators. In *Proc. Logic in Computer Science (LICS'2000)*. 30–41.

WINSKEL, G. 2005. Relations in concurrency. In *LICS*. IEEE Computer Society, 2–11.